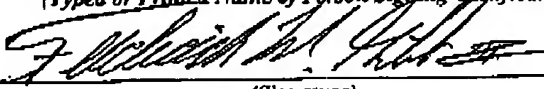


<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>			Docket No. <b>BUR920030092US1</b>
Applicant(s): <b>Allen et al.</b>			
Application No. <b>10/604,987</b>	Filing Date <b>08/29/03</b>	Examiner <b>Siek, Vuthe</b>	Group Art Unit <b>2822</b>
Invention: <b>THE USE OF A LAYOUT-OPTIMIZATION TOOL TO INCREASE THE YIELD AND RELIABILITY OF VLSI DESIGNS</b>			
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Application Number	10/604,987
Filing Date	08/29/03
First Named Inventor	Allen et al.
Art Unit	2822
Examiner Name	Siek, Yuthe
Attorney Docket Number	BUR920030092US1

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(Attorney or agent registration number)

  
Signature

4/6/05

Date

Frederick W. Gibb, III

Typed or printed name

301-261-8071

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